

GAU 2823
A11/a

GR 97 P 2681

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By: Mr. P. ~

Date: 7/12/00

7/19/00
Smith

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Albrecht Mayer

Appl. No. : 09/164,123

Filed : September 30, 1998

Title : Method for Assembling Integrated Circuits With Protection Of The Circuits Against Electrostatic Discharge, And Arrangement Of Integrated Circuits With Protection Against Electrostatic Discharge

Examiner : Kurt M. Eaton

Group Art Unit : 2823

A M E N D M E N T

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

S i r :

Responsive to the Office action dated April 13, 2000 kindly
amend the above-identified application as follows:

Please change the title to:

-- METHOD FOR ASSEMBLING INTEGRATED CIRCUITS WITH PROTECTION OF
THE CIRCUITS AGAINST ELECTROSTATIC DISCHARGE--

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